EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1136	(processor microprocessor cpu) near2 inactive	US-PGPUB; USPAT	OR	OFF	2006/03/17 14:29
L4	0	3 with voltage adj control	US-PGPUB; USPAT	OR	OFF	2006/03/17 14:29
L5	6	3 with (vrm voltage adj regulator dc adj converter)	US-PGPUB; USPAT	OR	OFF	2006/03/17 14:25
L6	2612	(processor microprocessor cpu) near2 (sleep suspend)	US-PGPUB; USPAT	OR	OFF	2006/03/17 14:29
L7	3	6 with voltage adj control	US-PGPUB; USPAT	OR	OFF	2006/03/17 14:30
L8	4	(3 6) same (spi "serial peripheral interface" inter adj ic iic "i.sup.2 c")	US-PGPUB; USPAT	OR	OFF	2006/03/17 14:38
L9	74	(inactive sleep suspend) with (spi "serial peripheral interface" inter adj ic iic "i.sup.2 c")	US-PGPUB; USPAT	OR	OFF	2006/03/17 14:42
L10	577	(vrm voltage adj regulator dc adj converter) with (sleep suspend standby)	US-PGPUB; USPAT	OR	OFF	2006/03/17 14:50
L11	93	10.clm.	US-PGPUB; USPAT	OR	OFF	2006/03/17 15:01
L12	54	(vrm voltage adj regulator dc adj converter) with (sleep suspend standby) near2 signal$1	US-PGPUB; USPAT	OR	OFF	2006/03/17 15:01
L13	7	12.clm.	US-PGPUB; USPAT	OR	OFF	2006/03/17 14:50
L14	68	voltage near control$4 with (sleep suspend standby) near2 signal$1	US-PGPUB; USPAT	OR	OFF	2006/03/17 15:00
L15	25	14.clm.	US-PGPUB; USPAT	OR	OFF	2006/03/17 15:00
L16	8	voltage near control$4 with (sleep suspend standby) near2 signal$1	EPO; JPO; IBM_TDB	OR	OFF	2006/03/17 15:00
L17	2	(vrm voltage adj regulator dc adj converter) with (sleep suspend standby) near2 signal$1	EPO; JPO; IBM_TDB	OR	OFF	2006/03/17 15:01
L18	2047	713/300.ccls.	US-PGPUB; USPAT	OR	OFF	2006/03/17 15:02
L19	22	18 and 10	US-PGPUB; USPAT	OR	OFF	2006/03/17 15:02
L20	2	18 and 14	US-PGPUB; USPAT	OR	OFF	2006/03/17 15:03
L21	51	18 and 3	US-PGPUB; USPAT	OR	OFF	2006/03/17 15:03